

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10044937	FILING DATE 01/15/2002	CLASS 438	SUBCLASS 121	CAU 212	EXAMINER FRANK
**APPLICANTS: Nakamura Gen; <div style="text-align: right;">2125</div> <div style="text-align: center;">700</div>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-014209 01/23/2001					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>ELF</i>				ATTORNEY DOCKET NO 03560.002982	
TITLE : Semiconductor manufacturing apparatus					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE Amount Due Date Paid		Total Claims Print Claim for O.G.	
		DRAWING Sheets Drwg. Figs. Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
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